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L10 and rank

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<u>L11</u>	L10 and rank	20	<u>L11</u>
<u>L10</u>	l9 and (module near3 bus)	257	<u>L10</u>
<u>L9</u>	L8 and (710/\$.ccls. or 711/\$.ccls.)	439	<u>L9</u>
<u>L8</u>	L7 and ((bus or clock or frequency or rate or speed or bandwidth) near4 (twice or times or greater or faster or slower or larger))	1009	<u>L8</u>
<u>L7</u>	L4 and (bus near8 (transfer\$4 or access\$4 or read\$4 or writ\$4 or stor\$4) near8 (bit or rate or speed or bandwidth))	1136	<u>L7</u>
<u>L6</u>	L5 and (memory near4 rank)	3	<u>L6</u>
<u>L5</u>	L4 and (bus near8 (bit or rate or speed or bandwidth))	1702	<u>L5</u>
<u>L4</u>	L3 and ((buffer or memory or module or bank) near8 port)	2159	<u>L4</u>
<u>L3</u>	L2 and l1	6498	<u>L3</u>
<u>L2</u>	bus near4 (module or DIMM or SIMM or card)	13514	<u>L2</u>
<u>L1</u>	memory near4 bus	41445	<u>L1</u>

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<u>L14</u>	L13 and (bus near8 ((clock or tim\$3 or rate or speed or bandwidth or width or wide\$2 or bit or byte or size or capacity) near4 (differen\$3 or fast\$2 or separate or slow\$2 or large\$2 or great\$2 or twice or more))))	6	<u>L14</u>
<u>L13</u>	(memory adj2 bus) with (read\$4 or writ\$4 or stor\$4 or transfer\$4 or load\$3) with (module adj2 bus)	13	<u>L13</u>

DB=USPT; PLUR=YES; OP=ADJ

<u>L12</u>	l11 and (bus near8 ((clock or tim\$3 or rate or speed or bandwidth or width or wide\$2 or bit or byte or size or capacity) near4 (differen\$3 or fast\$2 or separate or slow\$2 or large\$2 or great\$2 or twice or more))))	24	<u>L12</u>
<u>L11</u>	(memory adj2 bus) with (read\$4 or writ\$4 or stor\$4 or transfer\$4 or load\$3) with (module adj2 bus)	52	<u>L11</u>
<u>L10</u>	(register or buffer) with (block adj writ\$3) with l6	2	<u>L10</u>
<u>L9</u>	L8 and l7	7	<u>L9</u>
<u>L8</u>	L1 with (bit or byte) with (serial\$2 or consecutive\$2 or sequential\$2)	635	<u>L8</u>
<u>L7</u>	L6 with (bit or byte) with (parallel or simultaneous\$2)	100	<u>L7</u>
<u>L6</u>	module near4 bus	8360	<u>L6</u>
<u>L5</u>	l4 and ((rank or order or sequence) near8 port)	2	<u>L5</u>
<u>L4</u>	L3 and (module near4 bus)	53	<u>L4</u>
<u>L3</u>	(register or buffer) with (bit or byte) with (parallel or simultaneous\$2) with l1	268	<u>L3</u>
<u>L2</u>	(register or buffer) with (bit or parallel or simultaneous\$2) with l1	2945	<u>L2</u>
<u>L1</u>	(memory or system) near4 bus	65375	<u>L1</u>

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<u>L17</u>	(5717904 or 5781496).pn.	2	<u>L17</u>
<u>L16</u>	L15 and ((module or RAM or bank or DIMM) near8 port)	49	<u>L16</u>
<u>L15</u>	L14 and (memory near4 (control\$4 or arbit\$6))	107	<u>L15</u>
<u>L14</u>	L13 and ((module or RAM or bank or DIMM) near8 (rate or speed or bandwidth))	107	<u>L14</u>
<u>L13</u>	L12 and l1	376	<u>L13</u>
<u>L12</u>	block adj2 writ\$4	7644	<u>L12</u>
<u>L11</u>	L10 and ((module or RAM or bank or DIMM) near8 (rate or speed or bandwidth))	53	<u>L11</u>
<u>L10</u>	L9 same l7	329	<u>L10</u>
<u>L9</u>	(even or odd or high\$2 or low\$2) adj2 address	13663	<u>L9</u>
<u>L8</u>	L7 and l4 and l5	21	<u>L8</u>
<u>L7</u>	bus near8 (bit or parallel or burst or DDR or ((double or dual or twice) adj2 (rate or speed))) near8 (transfer\$4 or writ\$4 or stor\$4)	13141	<u>L7</u>
<u>L6</u>	L5 and l4 and l1	13	<u>L6</u>
<u>L5</u>	L2 near8 (rate or speed or bandwidth)	1009	<u>L5</u>
<u>L4</u>	L3 near8 (rate or speed or bandwidth)	122	<u>L4</u>
<u>L3</u>	module adj2 bus	2930	<u>L3</u>
<u>L2</u>	memory adj2 bus	14890	<u>L2</u>
<u>L1</u>	bus near8 (parallel or burst or DDR or ((double or dual or twice) adj2 (rate or speed))) near8 (transfer\$4 or writ\$4 or stor\$4)	4308	<u>L1</u>

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<u>L21</u>	L20 and ((module near4 port) with (memory adj2 bus))	5	<u>L21</u>
<u>L20</u>	L19 and l3	210	<u>L20</u>
<u>L19</u>	L18 and (bit near4 (rank or bank or RAM or buffer))	7583	<u>L19</u>
<u>L18</u>	((transfer\$4 or writ\$3 or stor\$3) near8 (burst or parallel or DDR))	58513	<u>L18</u>
<u>L17</u>	((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 (burst or parallel or DDR))	77739	<u>L17</u>
<u>L16</u>	L15 and l3	6	<u>L16</u>
<u>L15</u>	l11 and ((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 (burst or parallel or DDR))	97	<u>L15</u>
<u>L14</u>	l12 and l3	6	<u>L14</u>
<u>L13</u>	L12 and l8	0	<u>L13</u>
<u>L12</u>	l11 and ((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 (bus or clock or rate or speed or frequency or bandwidth) near4 (double or greater or faster or slower or larger or multiple or twice or ((two or three or four) adj2 time)))	91	<u>L12</u>
<u>L11</u>	((710/307)!.CCLS.)	224	<u>L11</u>
<u>L10</u>	L9 and l3	5	<u>L10</u>
<u>L9</u>	L8 and ((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 (bus or clock or rate or speed or frequency or bandwidth) near4 (double or greater or faster or slower or larger or multiple or twice or ((two or three or four) adj2 time)))	68	<u>L9</u>
<u>L8</u>	rank near8 (module or memory or port)	856	<u>L8</u>
<u>L7</u>	L6 and ((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 ((bus or clock or rate or speed or frequency or bandwidth) near4 (double or greater or faster or slower or larger or multiple or twice or ((two or three or four) adj2 time))))	43	<u>L7</u>
<u>L6</u>	rank near8 (memory or module)	744	<u>L6</u>
<u>L5</u>	L4 and (module near4 port)	62	<u>L5</u>
<u>L4</u>	L3 and ((transfer\$4 or read\$3 or writ\$3 or access\$3 or stor\$3) near8 (bus or clock or rate or speed or frequency or bandwidth) near4 (double or greater or faster or slower or larger or multiple or twice or ((two or three or four) adj2 time)))	306	<u>L4</u>
<u>L3</u>	L2 and (module adj2 bus)	809	<u>L3</u>
<u>L2</u>	L1 and (memory adj2 bus)	11780	<u>L2</u>
<u>L1</u>	bus near8 (control\$4 or arbit\$4)	72532	<u>L1</u>

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<u>L20</u>	l3 with ((parallel near4 (transfer\$4 or writ\$4 or stor\$4 or read\$3)) or (block adj writ\$4) or ddr or (double adj data adj rate))	10	<u>L20</u>
<u>L19</u>	l3 with ((parallel near4 (transfer\$4 or writ\$4 or stor\$4 or read\$3)) or (block adj writ\$4))	9	<u>L19</u>
<u>L18</u>	l2 with ((serial\$3 or sequential\$3) near4 (transfer\$4 or writ\$4 or stor\$4 or read\$3))	109	<u>L18</u>
<u>L17</u>	(5717904 or 5781496).pn.	2	<u>L17</u>
<u>L16</u>	L15 and ((module or RAM or bank or DIMM) near8 port)	49	<u>L16</u>
<u>L15</u>	L14 and (memory near4 (control\$4 or arbit\$6))	107	<u>L15</u>
<u>L14</u>	L13 and ((module or RAM or bank or DIMM) near8 (rate or speed or bandwidth))	107	<u>L14</u>
<u>L13</u>	L12 and l1	376	<u>L13</u>
<u>L12</u>	block adj2 writ\$4	7644	<u>L12</u>
<u>L11</u>	L10 and ((module or RAM or bank or DIMM) near8 (rate or speed or bandwidth))	53	<u>L11</u>
<u>L10</u>	L9 same l7	329	<u>L10</u>
<u>L9</u>	(even or odd or high\$2 or low\$2) adj2 address	13663	<u>L9</u>
<u>L8</u>	L7 and l4 and l5	21	<u>L8</u>
<u>L7</u>	bus near8 (bit or parallel or burst or DDR or ((double or dual or twice) adj2 (rate or speed))) near8 (transfer\$4 or writ\$4 or stor\$4)	13141	<u>L7</u>
<u>L6</u>	L5 and l4 and l1	13	<u>L6</u>
<u>L5</u>	L2 near8 (rate or speed or bandwidth)	1009	<u>L5</u>
<u>L4</u>	L3 near8 (rate or speed or bandwidth)	122	<u>L4</u>
<u>L3</u>	module adj2 bus	2930	<u>L3</u>
<u>L2</u>	memory adj2 bus	14890	<u>L2</u>
<u>L1</u>	bus near8 (parallel or burst or DDR or ((double or dual or twice) adj2 (rate or speed))) near8 (transfer\$4 or writ\$4 or stor\$4)	4308	<u>L1</u>

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or wide or bandwidth) <sentence>
(module data bus)
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The following characters have specialized meaning:

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transfer* or load*) <sentence>
(module bus)
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